

ECE395: Lab 3 Pre-Lab

Names: _____

Date: _____

Group: _____

FLA Switch – Pin Assignment _____

| | Register Name | Register Address | Value |
|-----------------------------------|---------------|------------------|------------------------|
| Port Pin Control Register | | | Initialization: |
| GPIO Port Data Direction Register | | | Mask: |
| GPIO Port Data Input Register | | | |

OLA Switch – Pin Assignment _____

| | Register Name | Register Address | Value |
|-----------------------------------|---------------|------------------|------------------------|
| Port Pin Control Register | | | Initialization: |
| GPIO Port Data Direction Register | | | Mask: |
| GPIO Port Data Input Register | | | |

ACK Pushbutton – Pin Assignment _____

| | Register Name | Register Address | Value |
|-----------------------------------|---------------|------------------|------------------------|
| Port Pin Control Register | | | Initialization: |
| GPIO Port Data Direction Register | | | Mask: |
| GPIO Port Data Input Register | | | |

TST Pushbutton – Pin Assignment _____

| | Register Name | Register Address | Value |
|-----------------------------------|---------------|------------------|------------------------|
| Port Pin Control Register | | | Initialization: |
| GPIO Port Data Direction Register | | | Mask: |
| GPIO Port Data Input Register | | | |

Green LED – Pin Assignment _____

| | Register Name | Register Address | Value |
|-----------------------------------|---------------|------------------|------------------------|
| Port Pin Control Register | | | Initialization: |
| GPIO Port Data Direction Register | | | Mask: |
| GPIO Port Data Output Register | | | |
| GPIO Port Clear Output Register | | | |
| GPIO Port Set Output Register | | | |
| GPIO Port Toggle Output Register | | | |

Yellow LED – Pin Assignment _____

| | Register Name | Register Address | Value |
|-----------------------------------|---------------|------------------|------------------------|
| Port Pin Control Register | | | Initialization: |
| GPIO Port Data Direction Register | | | Mask: |
| GPIO Port Data Output Register | | | |
| GPIO Port Clear Output Register | | | |
| GPIO Port Set Output Register | | | |
| GPIO Port Toggle Output Register | | | |

Red LED – Pin Assignment _____

| | Register Name | Register Address | Value |
|-----------------------------------|---------------|------------------|------------------------|
| Port Pin Control Register | | | Initialization: |
| GPIO Port Data Direction Register | | | Mask: |
| GPIO Port Data Output Register | | | |
| GPIO Port Clear Output Register | | | |
| GPIO Port Set Output Register | | | |
| GPIO Port Toggle Output Register | | | |